



SUMMER TRAINEE AND MASTER'S THESIS POSITIONS – JOIN US!

We have three open positions for Summer Trainees and one Master's Thesis worker at ST-Ericsson Turku. Summer Trainees will join RF/analog/digital IC design teams. Master's Thesis worker will be part of RF IC team.

Education level: For Summer Trainees: minimum 3rd year students targeting to Master's or Bachelor's degree in electronics, signal processing or digital design. For master's thesis work position: studies at final phase.

Years of work experience: previous work experience on area not required but is an advantage.

RF summer trainee tasks can be found in design, verification or measurement areas. In ASIC design tasks can be HW oriented tasks such as VHDL creation, simulation, synthesis and specification. In case you are more SW oriented person, we are able to offer embedded SW oriented tasks such as test SW creation.

Other requirements:

We appreciate good success at studies and interest in IC development. Proficient in circuit simulators (eldo, spectre), matlab, VHDL, system C or C++ is a plus. Or knowledge on synthesis or FPGA.

We expect you to be fluent in English. We appreciate also good communication and interpersonal skills as well as initiative and team working attitude.

Are you interested?

Please send your application, CV and transcript of records by February 28th to lotta.lehtonen@stericsson.com

For more information please contact Lotta Lehtonen at 020 728 3031 or 0400 590 891.

ST-Ericsson is a leading global designer, developer and fabless creator of platforms and semiconductors for wireless devices. The company is a 50/50 joint venture created by bringing together ST-NXP Wireless and Ericsson Mobile Platforms, two of the world's leading mobile semiconductor and platform companies, both with strong industry heritage. ST-Ericsson has approximately 6500 employees in Europe, Asia, America and Africa. In Finland the company has R&D sites in Turku, Tampere and Oulu.